

**Symbolic  
Reachability Analysis**

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## Reference

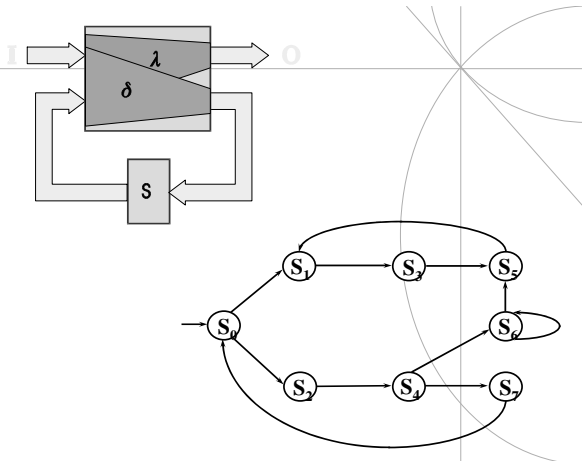
- ❖ Paper
- ❖ Books
  - C. Meinel, T. Theobald  
"Algorithms and Data Structure in VLSI Design"  
Springer-Verlag, Berlin, August 1998  
ISBN 3-540-64486-5
  - G. D. Hachtel, F. Somenzi  
"Logic Synthesis and Verification Algorithms"  
Kluwer Academic Publishers

## Outline

- ❖ Background
  - ◆ FSM Model and State Space Graph Representation
  - ◆ State Space Visit: DFS and BFS Paradigms
  - ◆ Functions and Sets Representation
  - ◆ Image Computation Concepts
  - ◆ Impact and Reference
- ❖ Transition Relation
- ❖ Image Computation
- ❖ Reachability Analysis
- ❖ Limits

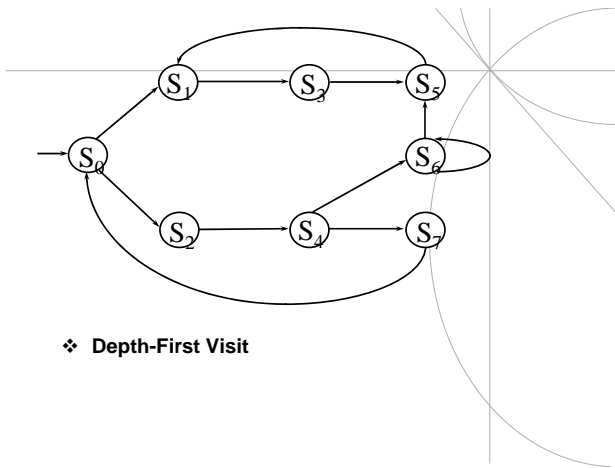
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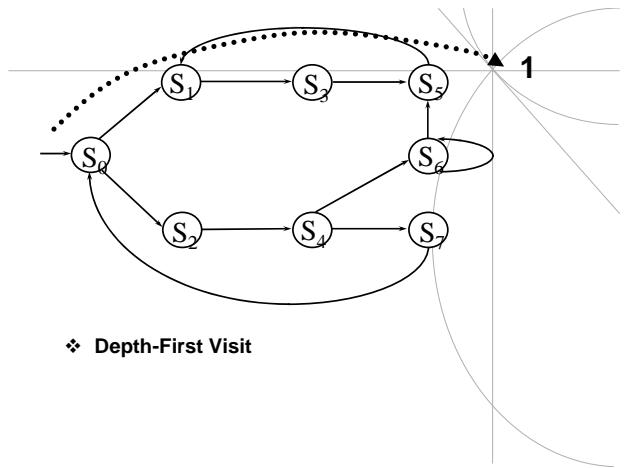


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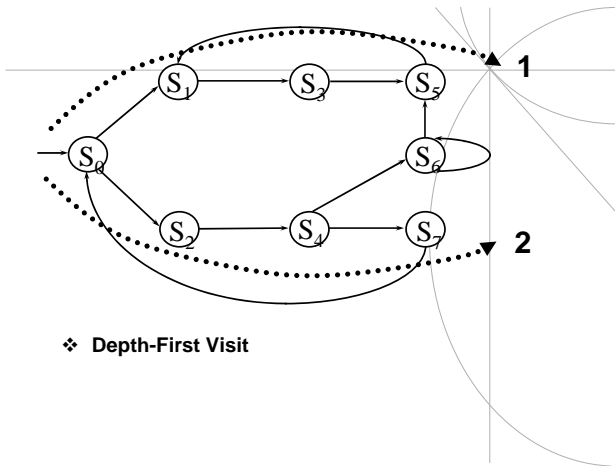
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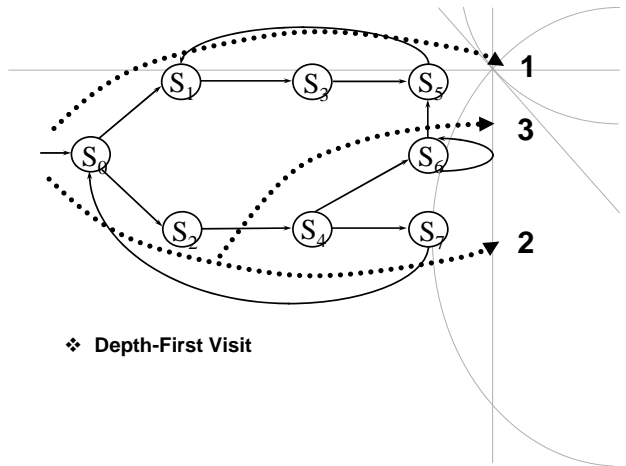
❖ Depth-First Visit



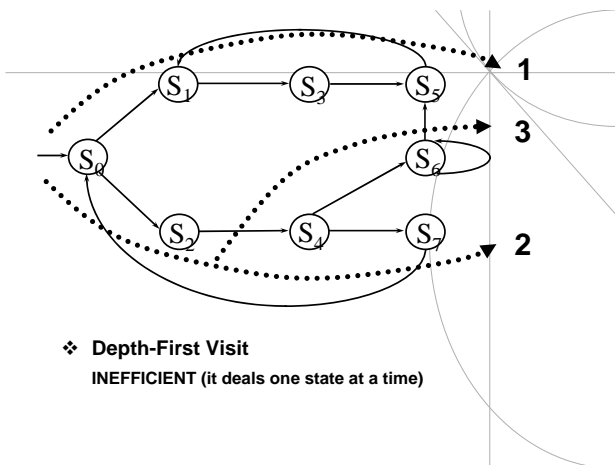
❖ Depth-First Visit



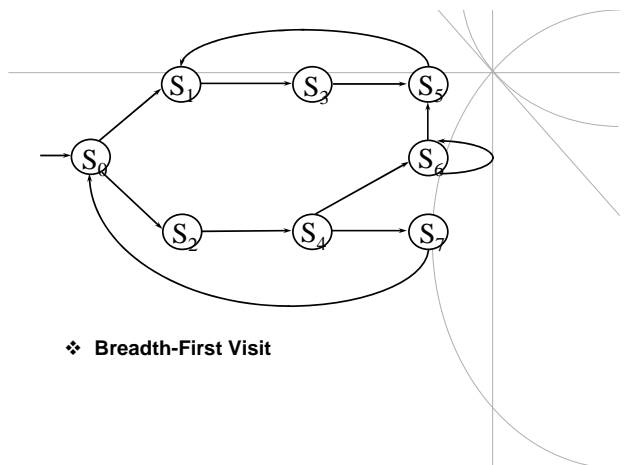
❖ Depth-First Visit



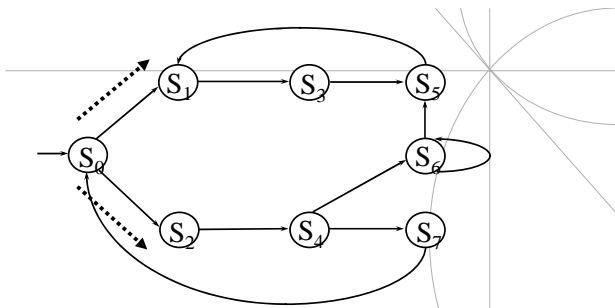
❖ Depth-First Visit



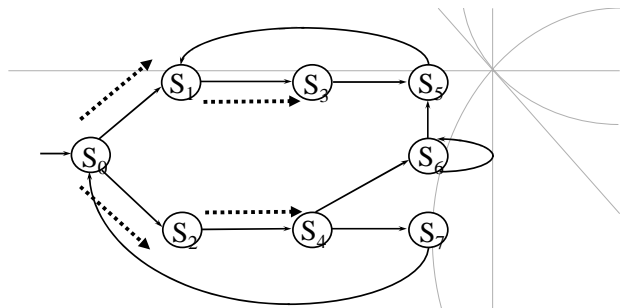
❖ Depth-First Visit  
INEFFICIENT (it deals one state at a time)



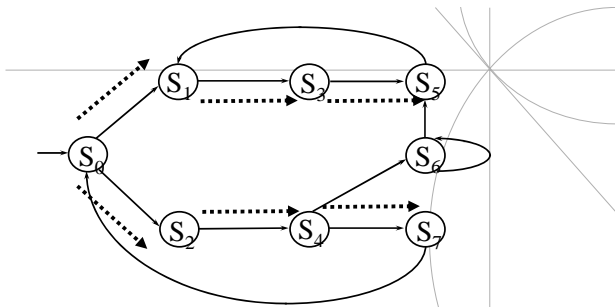
❖ Breadth-First Visit



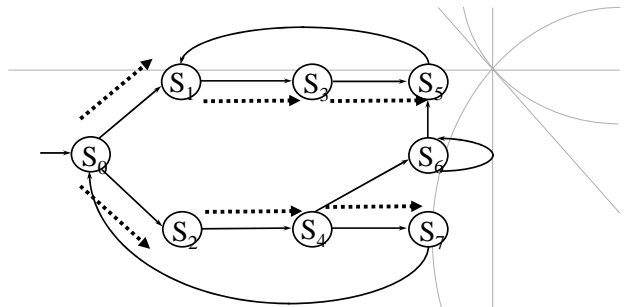
❖ Breadth-First Visit



❖ Breadth-First Visit

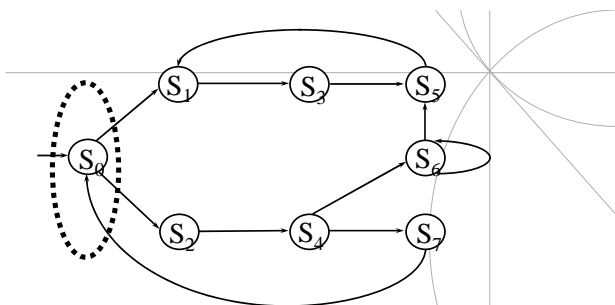


❖ Breadth-First Visit



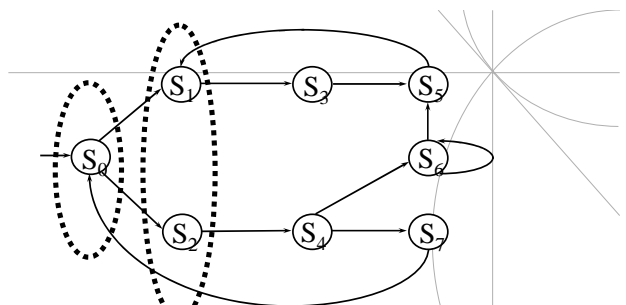
❖ Breadth-First Visit

EFFICIENT IFF we can deal with multiple states (set of state)



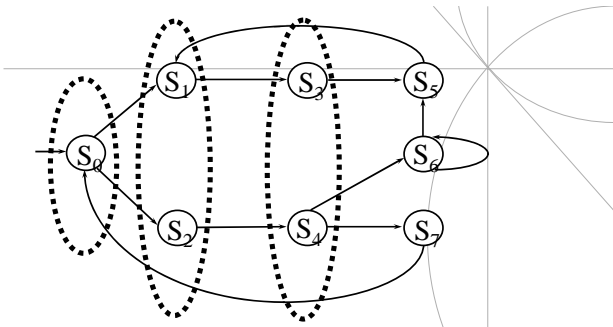
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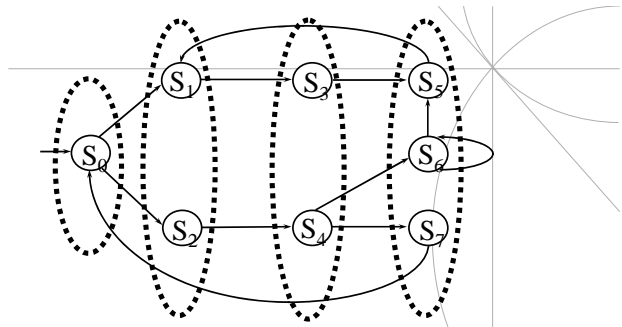
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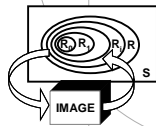
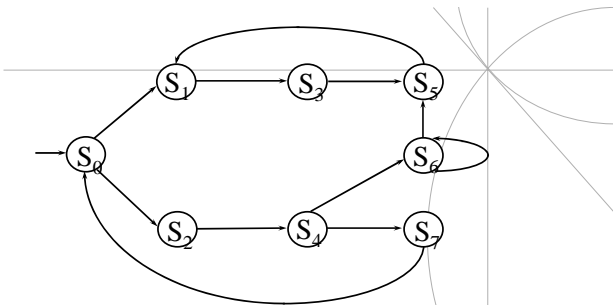
❖ **Breadth-First Visit**

EFFICIENT IFF we can deal with multiple states (set of state)



❖ **Breadth-First Visit**

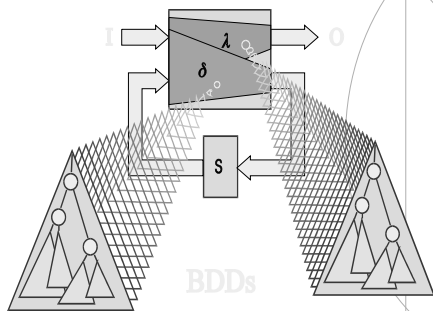
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- ❖ Limits

**Function Representation**



**Set Representation**

- ❖ Idea
  - A formula can represent a set of states (its models)
- ❖ Example
  - $(x \oplus y) \oplus z$
  - represents  $\{100,010,110,111\}$



**Characteristic Function**

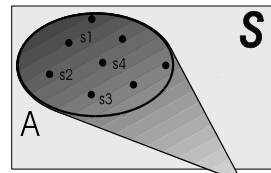
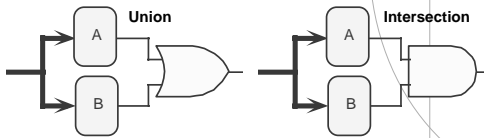
❖  $A \subseteq \{0,1\}^n$   
(Set of bit vectors of length  $n$ )

❖ Represent set  $A$  as Boolean function  $A$  of  $n$  variables

$X \in A$  if and only if  $A(X) = 1$



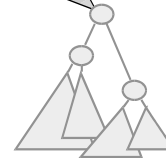
**Set Operations**



Characteristic Function of set  $A$ :

$$\chi_A(s) = 1 \text{ IFF } s \in A$$

$$= 0 \text{ IFF } s \notin A$$

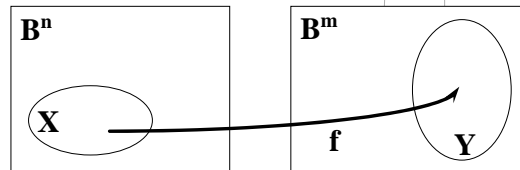


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**Image and inverse image**

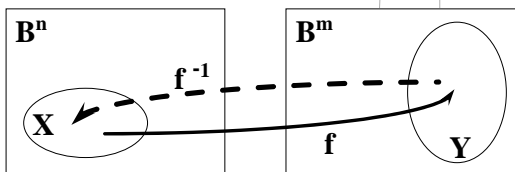
$$\text{Img}(f, X) = f(X) = \{ y \in B^m \mid x \in X \wedge y = f(x) \}$$



**Image and inverse image**

$$\text{Img}(f, X) = f(X) = \{ y \in B^m \mid x \in X \wedge y = f(x) \}$$

$$\text{PreImg}(f, Y) = f^{-1}(Y) = \{ x \in B^n \mid y \in Y \wedge y = f(x) \}$$



**FSM Analysis Impact**

- ❖ Systems Represented as Finite State Machines
  - ◆ Sequential circuits
  - ◆ Communication protocols
  - ◆ Synchronization programs
- ❖ Analysis Tasks
  - ◆ State reachability
  - ◆ State machine comparison
  - ◆ Temporal logic model checking
- ❖ Traditional Methods Impractical for Large Machines
  - ◆ Polynomial in number of states
  - ◆ Number of states exponential in number of state variables
  - ◆ Example: single 32-bit register has 4,294,967,296 states!

## A Few Related Works

- ❖ Ranjan & co. IWLS-1995:
  - ◆ Clustering and ordering heuristics most widely used
- ❖ Hojati & co. ICCD-1996:
  - ◆ Theoretic results
- ❖ Moon & co. DAC-2000 & FMCAD-2000:
  - ◆ Transition function VS transition relation
  - ◆ Active lifetime – dependence matrix
- ❖ Gupta & co. FMCAD-2000:
  - ◆ BDD and SAT for computing images in traversal
- ❖ Meinel & co. FMCAD-2000, ICCD-2001:
  - ◆ Using hierarchical information for conjunction scheduling

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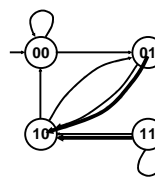
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## The Transition Relation

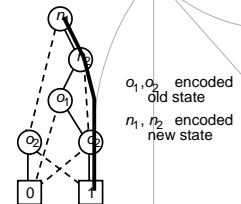
$$TR(s, x, y) = \prod_{i=1}^n (y_i \equiv \delta_i(s, x))$$

The Transition Relation expresses present-state, primary input  $\Rightarrow$  next state correspondence.

### Deterministic FSM



### Symbolic Representation

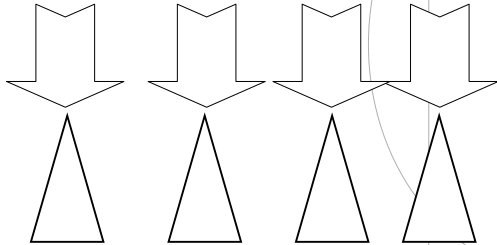


- ◆ Represent set of transitions as function  $\delta(Old, New)$ 
  - ✦ Yields 1 if can have transition from state *Old* to state *New*
- ◆ Represent as Boolean function
  - ✦ Over variables encoding states

$$TR(s,x,y) = \prod_{i=1}^n (y_i \equiv \delta_i(s,x))$$

$$TR(s,x,y) = \prod_{i=1}^n (y_i \equiv \delta_i(s,x)) = [(y_1 \equiv \delta_1(s,x)) \cdot (y_2 \equiv \delta_2(s,x)) \cdot \dots \cdot (y_n \equiv \delta_n(s,x))]$$

$$\begin{aligned}
 TR(s,x,y) &= \\
 &= \prod_{i=1}^n (y_i \equiv \delta_i(s,x)) \\
 &= [ (y_1 \equiv \delta_1(s,x)) \cdot (y_2 \equiv \delta_2(s,x)) \cdot \dots \cdot (y_n \equiv \delta_n(s,x)) ]
 \end{aligned}$$



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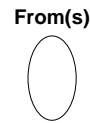
## Image Computation

$$Img(TR, From) = \exists_{s,x} [TR(s, x, y) \cdot From(s)]$$

Image is computed through:  
a conjunction-abstraction operation between present state set and transition relation.

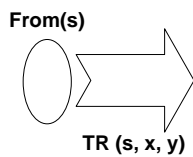
## Image Computation

$$Img(TR, From) = \exists_{s,x} [TR(s, x, y) \cdot From(s)]$$



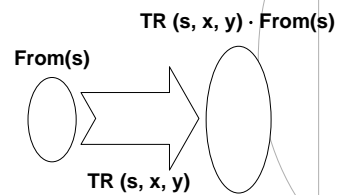
## Image Computation

$$Img(TR, From) = \exists_{s,x} [TR(s, x, y) \cdot From(s)]$$



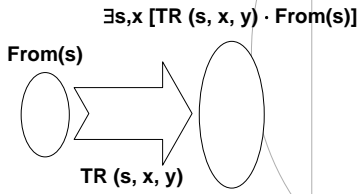
## Image Computation

$$Img(TR, From) = \exists_{s,x} [TR(s, x, y) \cdot From(s)]$$



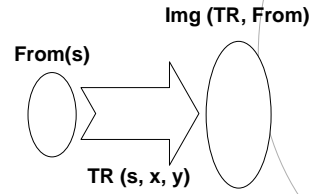
### Image Computation

$$\text{Img (TR, From)} = \exists_{s,x} [\text{TR (s, x, y)} \cdot \text{From(s)}]$$



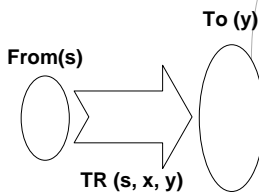
### Image Computation

$$\text{Img (TR, From)} = \exists_{s,x} [\text{TR (s, x, y)} \cdot \text{From(s)}]$$



### Image Computation

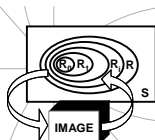
$$\text{To (y)} = \text{Img (TR, From)} = \exists_{s,x} [\text{TR (s, x, y)} \cdot \text{From(s)}]$$



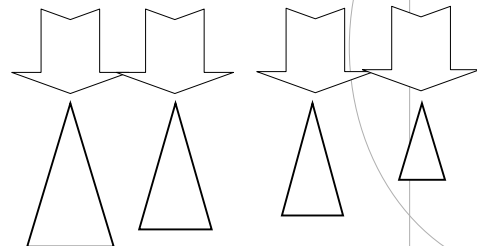
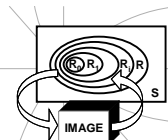
$$\begin{aligned} \text{To (y)} &= \\ &= \exists_{s,x} [\text{TR (s,x,y)} \cdot \text{From (s)}] \end{aligned}$$



$$\begin{aligned} \text{To (y)} &= \\ &= \exists_{s,x} [\text{TR (s,x,y)} \cdot \text{From (s)}] \\ &= \exists_{s,x} [ (y_1 \equiv \delta_1) \cdot (y_2 \equiv \delta_2) \cdot \dots \cdot (y_n \equiv \delta_n) \cdot \text{From (s)} ] \end{aligned}$$

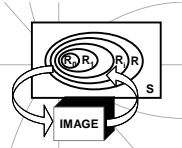
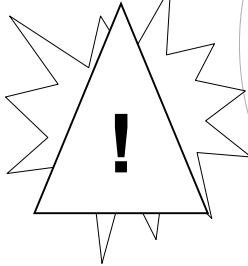


$$\begin{aligned} \text{To (y)} &= \\ &= \exists_{s,x} [\text{TR (s,x,y)} \cdot \text{From (s)}] \\ &= \exists_{s,x} [ (y_1 \equiv \delta_1) \cdot (y_2 \equiv \delta_2) \cdot \dots \cdot (y_n \equiv \delta_n) \cdot \text{From (s)} ] \end{aligned}$$





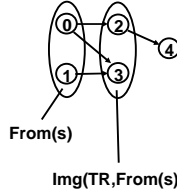
$$\begin{aligned}
 \text{To}(y) &= \\
 &= \exists_{sx} [ \text{TR}(s, x, y) \cdot \text{From}(s) ] \\
 &= \exists_{sx} [ (y_1 \equiv \delta_1) \cdot (y_2 \equiv \delta_2) \cdot \dots \cdot (y_n \equiv \delta_n) \cdot \text{From}(s) ]
 \end{aligned}$$



## Image and Pre-Image of States: An Example

Image of a set of states From(s)

Example:



$$\begin{aligned}
 \text{From}(s) &= (s = 0) \vee (s = 1) && \{0, 1\} \\
 \text{TR}(s, y) &= \\
 &= (s = 0) \wedge (y = 2) \vee && \{(0, 2), \\
 &= (s = 0) \wedge (y = 3) \vee && (0, 3), \\
 &= (s = 1) \wedge (y = 3) \vee && (1, 3), \\
 &= (s = 2) \wedge (y = 4) && (2, 4)\} \\
 \text{TR}(s, y) \wedge \text{From}(s) &= \\
 &= (s = 0) \wedge (y = 2) \vee && \{(0, 2), \\
 &= (s = 0) \wedge (y = 3) \vee && (0, 3), \\
 &= (s = 1) \wedge (y = 3) && (1, 3)\} \\
 \text{To}(y) = \exists s (\text{TR} \wedge \text{From}) &= \\
 &= (y = 2) \vee (y = 3) && \{(2, 3)\}
 \end{aligned}$$

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## Representations

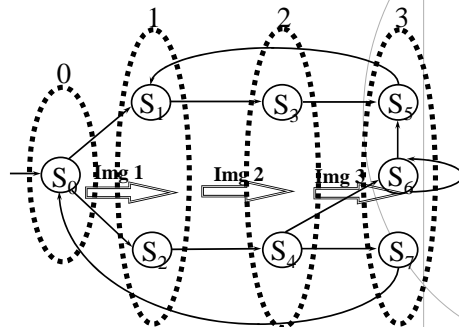
- ❖ Explicit reachability analysis
  - ◆ Represent states explicitly (e.g. as bit string) => limited capacity
  - ◆ Use hashtable to find quickly whether state was reached before
  - ◆ Image operation: simple simulation
  - ◆ Preimage operation: SAT run
- ❖ Symbolic reachability analysis
  - ◆ Represent states and transition relation symbolically
    - ✦ E.g. BDDs, circuits, DNF, etc.
  - ◆ Use BDD operations to perform image and preimage operation (simple AND or AND\_EXIST)
  - ◆ Lots of heuristic improvements to keep BDD size under control

## State Traversal Techniques

- ❖ Forward Traversal
  - ◆ Start from initial state(s)
  - ◆ Traverse forward to check whether "bad"
  - ◆ State(s) is reachable
- ❖ Backward Traversal
  - ◆ Start from bad state(s)
  - ◆ Traverse backward to check whether initial
  - ◆ State(s) can reach them
- ❖ Combines Forward/Backward traversal
  - ◆ compute over-approximation of reachable states by forward traversal
  - ◆ for all bad states in over-approximation, start backward traversal to see whether initial state can reach them

## Forward Reachability Analysis (Forward Traversal)

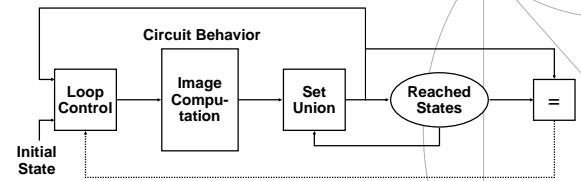
Sequence of image computations ... till fixed point ...



**FwdTraversal (TR, S<sub>0</sub>)**

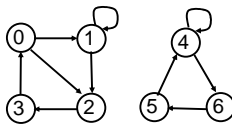
```

Reached = From = New = S0 (s)
while ( New ≠ ∅ )
  To = Img (TR, From)
  To |y→s
  New = To ∧ ¬Reached
  Reached = Reached ∨ New
  From = Best_BDD (New, Reached)
return (Reached (s))
    
```



- ◆ Determine set of all reachable states of circuit
- ◆ Key step in model checking
  - ◇ Many (but not all) properties can be checked by some form of reachability computation

❖ Example



Iteration:	1	2	3
From:	{0}	{1,2}	{1,2,3}
To:	{1,2}	{1,2,3}	{0,1,2,3}
Reached:	{0}	{0,1,2}	{0,1,2,3}

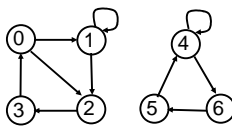
**Backward State Traversal**

**BwdTraversal (TR, S<sub>0</sub>)**

```

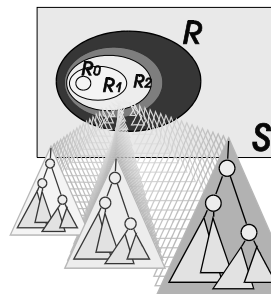
Reached = From = New = S0 (s)
while ( New ≠ ∅ )
  To = Prelmg (TR, From)
  To |y→s
  New = To ∧ ¬Reached
  Reached = Reached ∨ New
  From = Best_BDD (New, Reached)
return (Reached (s))
    
```

❖ Example



Iteration:	1	2	3
From (current):	{6}	{4}	{4,5}
To (previous):	{4}	{4,5}	{4,5,6}
Reached:	{6}	{4,6}	{4,5,6}

**To summarise**



**Forward Traversal**

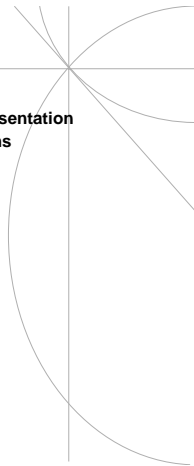
$R_0 = \text{Initial State Set}$   
 $R_{i+1} = R_i + \text{Img} (TR, R_i)$

**Backward Traversal**

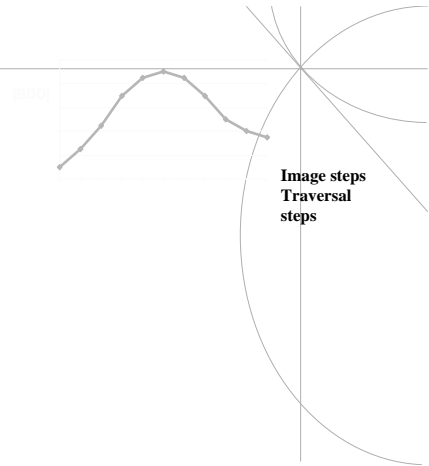
$R_0 = \text{Initial State Set}$   
 $R_{i+1} = R_i + \text{Prelmg} (TR, R_i)$

## Outline

- ❖ Background
  - ◆ FSM Model and State Space Graph Representation
  - ◆ State Space Visit: DFS and BFS Paradigms
  - ◆ Functions and Sets Representation
  - ◆ Image Computation Concepts
  - ◆ Impact and Reference
- ❖ Transition Relation
- ❖ Image Computation
- ❖ Reachability Analysis
- ❖ Limits



Maximum Size at intermediate steps  
[Ravi & Somenzi, ICCAD'95]



Maximum Size at intermediate steps  
[Ravi & Somenzi, ICCAD'95]

